

IN THE CLAIMS

Please amend the claims as follows:

1-4. (Canceled)

5. (Currently Amended) An interconnect forming method comprising:

a step of forming a barrier film for metal diffusion on an insulator base;

a step of selectively forming a metal seed layer on the barrier film for metal diffusion
using an electroless plating process; and

a step of selectively forming a metal conductive layer on the metal seed layer using an
electroplating process.

6. (Original) An interconnect forming method comprising:

a step of forming a barrier film for metal diffusion on an insulator base;

a step of selectively forming a metal seed layer on the barrier film for metal diffusion
using an electroless plating process;

a step of selectively forming a metal conductive layer on the metal seed layer using an
electroplating process; and

a step of etching the barrier film for metal diffusion using the metal conductive layer
as a mask.

7. (Original) An interconnect forming method comprising:

a step of forming a barrier film for metal diffusion on an insulator base;

a step of selectively forming a metal seed layer on the barrier film for metal diffusion
using an electroless plating process;

a step of etching the barrier film for metal diffusion using the metal seed layer as a

mask; and

a step of selectively forming a metal conductive layer on the metal seed layer using an electroplating process.

8. (Original) The interconnect forming method according to claim 5, wherein the insulator base has a substrate and an underlying insulator film provided on the substrate.

9. (Currently Amended) ~~The~~ An interconnect forming method ~~according to claim 5~~ comprising:

a step of forming a barrier film for metal diffusion on an insulator base;

a step of selectively forming a metal seed layer on the barrier film for metal diffusion;

and

a step of selectively forming a metal conductive layer on the metal seed layer using an electroplating process,

further comprising a step of carrying out annealing after the metal seed layer has been formed to reduce film stress that may occur in the metal seed layer.

10. (Currently Amended) ~~The~~ An interconnect forming method ~~according to claim 5~~ comprising:

a step of forming a barrier film for metal diffusion on an insulator base;

a step of selectively forming a metal seed layer on the barrier film for metal diffusion;

and

a step of selectively forming a metal conductive layer on the metal seed layer using an electroplating process,

wherein the step of selectively forming the metal seed layer on the barrier film for

metal diffusion using the electroless plating process uses a mask composed of a photosensitive resin and having a trench shaped so as to correspond to an area in which the metal seed layer is formed, to execute the electroless plating process to form the metal seed layer on the barrier film for metal diffusion exposed from this trench.

11.-14. (Canceled)

15. (New) The interconnect forming method according to claim 6, wherein the insulator base has a substrate and an underlying insulator film provided on the substrate.

16. (New) The interconnect forming method according to claim 6, further comprising a step of carrying out annealing after the metal seed layer has been formed to reduce film stress that may occur in the metal seed layer.

17. (New) The interconnect forming method according to claim 6, wherein the step of selectively forming the metal seed layer on the barrier film for metal diffusion using the electroless plating process uses a mask composed of a photosensitive resin and having a trench shaped so as to correspond to an area in which the metal seed layer is formed, to execute the electroless plating process to form the metal seed layer on the barrier film for metal diffusion exposed from this trench.

18. (New) The interconnect forming method according to claim 7, wherein the insulator base has a substrate and an underlying insulator film provided on the substrate.

19. (New) The interconnect forming method according to claim 7, further comprising a step of carrying out annealing after the metal seed layer has been formed to reduce film stress that may occur in the metal seed layer.

20. (New) The interconnect forming method according to claim 7, wherein the step

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of selectively forming the metal seed layer on the barrier film for metal diffusion using the electroless plating process uses a mask composed of a photosensitive resin and having a trench shaped so as to correspond to an area in which the metal seed layer is formed, to execute the electroless plating process to form the metal seed layer on the barrier film for metal diffusion exposed from this trench.